

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Dougherty et al.

Examiner: P. Dinh

Serial No: 10/780,140

Group Art Unit: 2825

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Docket: YOR920030437US1
(8728-653)

For: METHOD FOR OPTIMIZATION OF LOGIC CIRCUITS FOR
ROUTABILITY IMPROVEMENT

Mail Stop RCE
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. § 1.111

Sir:

This is a response to the Advisory Action mailed May 29, 2007 and the Final Action mailed March 15, 2007. An RCE (request for continued examination) is filed herewith. The claims are amended as follows: